

Matter No.: 14225-044001

Applicant(s): Takeshi Suzuki et al.

HYBRID INTEGRATED CIRCUIT DEVICE

FIG.2

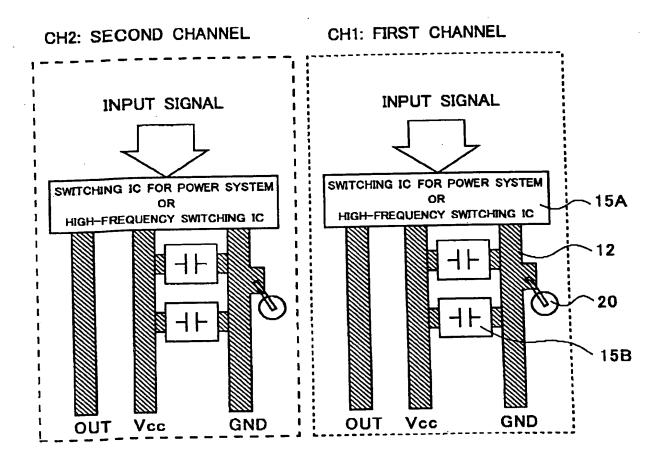


FIG.3

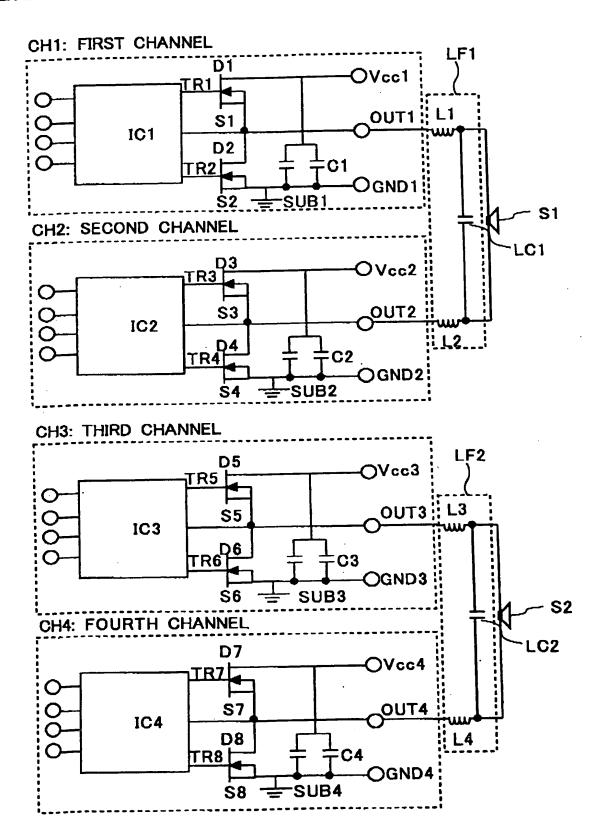


FIG.4A

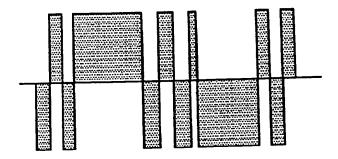


FIG.4B

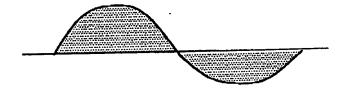


FIG.5A

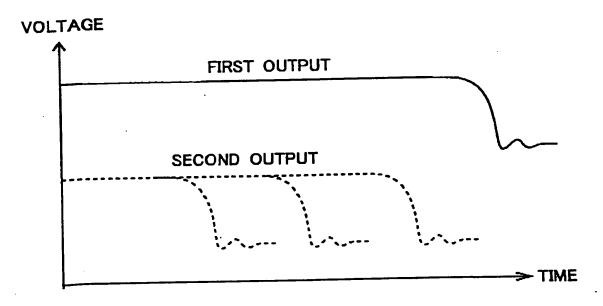


FIG.5B

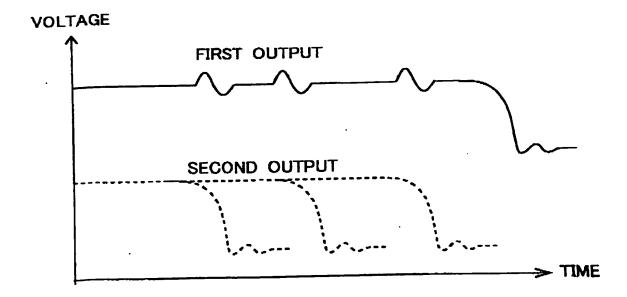


FIG.6A

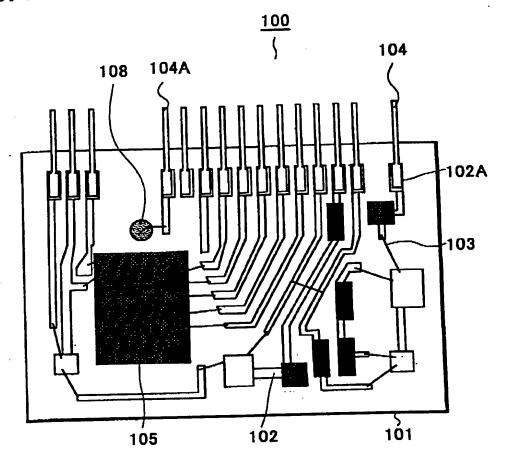


FIG.6B

